

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,167,059

Docket No: 10031136-01

Issue Date: January 23, 2007

Patentee: Robert Abraham et al.

Title Circuit for Generating Spread Spectrum Clock

REQUEST FOR CERTIFICATION OF CORRECTION UNDER 35 U.S.C. 255

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Office of Patent Publication
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 37 C.F.R. 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Deposit Account No. 57299.

Issuance of the Certificate of Correction would correct a typographical error but neither expand nor contract the scope of the claims as properly allowed. Re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No. 57299.

Please direct all correspondence to: Avago Technologies Limited
4380 Ziegler Road, MS: 76
Fort Collins, CO 80525

Respectfully submitted,

Date: February 4, 2008

By

Scott Weitzel

Reg. No.: 54,534

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page _____ of _____

PATENT NO. : 7,167,059

APPLICATION NO.: 10/820,691

ISSUE DATE : January 23, 2007

INVENTOR(S) : Scott R. Weaver, Robert Abraham.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 1, delete "(F_out)" and insert -- (F_out) --.
In Claim 7, delete "In" and insert -- in --.
In Claim 8, delete "fix" and insert -- for --.
In Claim 8, delete "second,transistor" and insert -- second transistor --.
In Claim 11, delete "(F_mod)" and insert -- (F_mod) --.
In Claim 11, delete "bite." and insert -- bits. --.
In Claim 16, delete "stop" and insert -- step --.
In Claim 20, delete "(PDCS)" and insert -- (PDCs) --.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Avago Technologies Limited, 4380 Ziegler Road, MS 76, Fort Collins, CO 80525

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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